

Day: Thursday Date: 3/27/2003 Time: 12:26:04

Inventor Name Search Result

Your Search was:

Last Name = WILCOX First Name = JEFFREY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08670858	5886557	150	06/28/1996	REDUNDANT CLOCK SIGNAL GENERATING CIRCUITRY	WILCOX, JEFFŘEY
08720102	6185634	150	09/27/1996	ADDRESS TRIGGERED DMA CONTROLLER WITH AN INDICATIVE SIGNAL INCLUDING CIRCUITRY FOR CALCULATING A NEW TRIGGER ADDRESS VALUE BASED ON THE SUM OF THE CURRENT TRIGGER ADDRESS AND THE DESCRIPTOR REGISTER DATA WITH A TRIGGER ADDRESS REGISTER	WILCOX, JEFFREY
09156747	6107855	150	09/17/1998	REDUNDANT CLOCK SIGNAL GENERATING CIRCUITY	WILCOX, JEFFREY
08438895	Not Issued	161	05/10/1995	COMPUTERIZED SYSTEM AND METHOD FOR CONDUCTING LOAN AND REAL ESTATE TRANSACTIONS	WILCOX , JEFFREY A.
08245763	Not Issued	161	05/18/1994	SYSTEM AND METHOD FOR COMPUTERIZED INTERACTION BETWEEN TWO INFORMATION SOURCES	WILCOX , JEFFREY A.
08278870	5594862	150	07/20/1994	XOR CONTROLLER FOR A STORAGE SUBSYSTEM	WILCOX , JEFFREY A.
08491467	6532547	150	06/16/1995	REDUNDANT PERIPHERAL DEVICE SUBSYSTEM	WILCOX , JEFFREY A.
08294481	5798961	150	08/23/1994	NON-VOLATILE MEMORY MODULE	WILCOX , JEFFREY A.

08277880	5586300	150	07/20/1994	FLEXIBLE ADDRESSING MEMORY CONTROLLER WHEREIN MULTIPLE MEMORY MODULES MAYBE ACCESSED ACCORDING TO COMPARISON OF CONFIGURATION ADDRESS	WILCOX , JEFFREY A.
09342347	6330639	150	06/29/1999	METHOD AND APPARATUS FOR DYNAMICALLY CHANGING THE SIZES OF POOLS THAT CONTROL THE POWER CONSUMPTION LEVELS OF MEMORY DEVICES	WILCOX , JEFFREY R.
06518082	4513347	150	07/28/1983	STATIC PROTECTIVE CHAIR	WILCOX , JEFFREY S.
07852472	D346282	150	03/17/1992	SIDE CHAIR	WILCOX , JEFFREY S.
07031391	4753409	150	03/27/1987	CHAIR SUPPORT INCORPORATING A HEIGHT ADJUSTMENT MECHANISM	WILCOX , JEFFREY S.
29092710	D412259	150	08/25/1998	FISHING SEAT	WILCOX , JEFFREY S.
07852470	Not Issued	161	03/17/1992	CHAIR	WILCOX , JEFFREY S.
08192294	Not Issued	161	02/04/1994	MOBILE OFFICE VAN CONVERSION	WILCOX , JEFFREY S.
09162578	6089669	150	09/29/1998	FOLDING OUTDOOR SEAT	WILCOX , JEFFREY S.
09280728	6176459	150	03/29/1999	SELF-LOCKING ANGULARLY-TILTING SEAT SUPPORT	WILCOX , JEFFREY S.
29094296	D420816	150	09/29/1998	BOAT SEAT	WILCOX , JEFFREY S.
07410257	4987708	150	09/21/1989	SEISMIC ANCHOR	WILCOX , JEFFREY S.
07852473	D344649	150	03/17/1992	CHAIR SPINE	WILCOX , JEFFREY S.
06915718	4773122	150	10/03/1986	CONDUCTIVE CASTER	WILCOX , JEFFREY S.
07852471	Not Issued	161	03/17/1992	PEDESTAL CHAIR	WILCOX , JEFFREY S.

09652644	_ Not Issued	041	08/31/2000	ADAPTIVE DATA FETCH PREDICTION ALGORITHM	WILCOX, JEFFREY
09608159	Not Issued	030	06/30/2000	METHOD AND APPARATUS FOR AN INTEGRATED CIRCUIT HAVING FLEXIBLE-RATIO FREQUENCY DOMAIN CROSS-OVERS	WILCOX, JEFFREY R.
09966580	6510099	150	09/28/2001	MEMORY CONTROL WITH DYNAMIC DRIVER DISABLING	WILCOX, JEFFREY R.
10038960	_ Not Issued	020	01/02/2002	POWER REDUCTION IN A MEMORY BUS INTERFACE	WILCOX, JEFFREY R.
09963002	_ Not Issued	030	09/25/2001	MEMORY CONTROL WITH LOOKAHEAD POWER MANAGEMENT	WILCOX, JEFFREY R.
10133908	Not Issued	030	04/25/2002	LOW LATENCY BUFFER CONTROL SYSTEM AND METHOD	WILCOX, JEFFREY R.
10039087	Not Issued	030	01/03/2002	METHOD FOR DYNAMICALLY ADJUSTING A MEMORY PAGE CLOSING POLICY	WILCOX, JEFFREY R.
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10042862	6529442	150	01/08/2002	MEMORY CONTROLLER WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS	WILCOX, JEFFREY R.
10042862				WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS	1 ' 1
	_ Not Issued	019		WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS MEMORY SYSTEM THAT MEASURES POWER	JEFFREY R. WILCOX,
10331333	Not Issued Not Issued	019	12/31/2002	WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS MEMORY SYSTEM THAT MEASURES POWER CONSUMPTION INDIVIDUAL MEMORY PAGE ACTIVITY TIMING	WILCOX, JEFFREY R. WILCOX,
10331333	Not Issued Not Issued Not Issued Not Issued	019	12/31/2002 04/05/2002	WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS MEMORY SYSTEM THAT MEASURES POWER CONSUMPTION INDIVIDUAL MEMORY PAGE ACTIVITY TIMING METHOD AND SYSTEM APPARATUS AND METHOD FOR POWER EFFICIENT	WILCOX, JEFFREY R. WILCOX, JEFFREY R. WILCOX, JEFFREY R.
10331333 10116847 09968259	Not Issued Not Issued Not Issued 6357830	019 030 093	04/05/2002	WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS MEMORY SYSTEM THAT MEASURES POWER CONSUMPTION INDIVIDUAL MEMORY PAGE ACTIVITY TIMING METHOD AND SYSTEM APPARATUS AND METHOD FOR POWER EFFICIENT LINE DRIVER	WILCOX, JEFFREY R. WILCOX, JEFFREY R. WILCOX, JEFFREY R.
10331333 10116847 09968259	Not Issued Not Issued Not Issued 6357830 Not Issued	019 030 093 150 020	04/05/2002 09/28/2001 01/18/2000	WITH AC POWER REDUCTION THROUGH NON-RETURN-TO-IDLE OF ADDRESS AND CONTROL SIGNALS MEMORY SYSTEM THAT MEASURES POWER CONSUMPTION INDIVIDUAL MEMORY PAGE ACTIVITY TIMING METHOD AND SYSTEM APPARATUS AND METHOD FOR POWER EFFICIENT LINE DRIVER SHOCK ABSORBING SEAT PROSTHETIC MENISCUS AND METHOD OF	WILCOX, JEFFREY R. WILCOX, JEFFREY R. WILCOX, JEFFREY R. WILCOX, JEFFREY S.

					JEFFREY S.
60319657	Not Issued	020	10/29/2002	SEAT	WILCOX, JEFFREY S.
 60319180	Not Issued	020	04/11/2002	POWERED MARINE CLEAT AND PNEUMATIC SYSTEM	WILCOX, JEFFREY S.
 08287566	Not Issued	161		HIGH RESOLUTION PRECISION POSITIONING SYSTEM	WILCOXEN , JEFFREY L.
 08431512	5688867	150		LOW VOC UNSATURATED POLYESTER SYSTEMS AND USES THEREOF	WILCOXSON , JEFFREY L.
 <u>08916478</u>	<u>5874503</u>	150	=6		WILCOXSON , JEFFREY L.

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Day: Thursday Date: 3/27/2003 Time: 12:29:17

Inventor Name Search Result

Your Search was:

Last Name = SHEAFFER

First Name = GAD

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
08672100	6105124	150	06/27/1996	METHOD AND APPARATUS FOR MERGING BINARY TRANSLATED BASIC BLOCKS OF INSTRUCTIONS	SHEAFFER , GAD
08592761	Not Issued	161	01/26/1996	TRACE-BASED MERGING OF CODE BLOCKS TO FACILITATE CODE OPTIMIZATION IN BINARY TRANSLATION	SHEAFFER, GAD
09470971	6515672	150	12/23/1999	MANAGING PREFETCHING FROM A DATA BUFFER	SHEAFFER , GAD S
09466964	Not Issued	071	12/20/1999	BUFFER PRE-LOADING FOR MEMORY SERVICE INTERRUPTIONS	SHEAFFER , GAD S.
08657779	5784307	150	05/31/1996	NOVEL DIVISION ALGORITHM FOR FLOATING POINT OR INTEGER NUMBERS	SHEAFFER , GAD S.
09220159	6539471	150	12/23/1998	METHOD AND APPARATUS FOR PRE-PROCESSING INSTRUCTIONS FOR A PROCESSOR	SHEAFFER , GAD S.
08524065	5710902	150	09/06/1995	INSTRUCTION DEPENDENCY CHAIN INDENTIFIER	SHEAFFER , GAD S.
09334361	6470444	150	06/16/1999	METHOD AND APPARATUS FOR DIVIDING A STORE OPERATION INTO PRE-FETCH AND STORE MICRO-OPERATIONS	1 '11
09122270	Not Issued	161	07/24/1998	ADVANCED REGISTER RENAMING SYSTEM FOR IMPROVED INSTRUCTION LEVEL PARALLELISM	SHEAFFER , GAD S.
08582195	Not Issued	169	01/02/1996	COUNTER BASED BRANCH PREDICTION	SHEAFFER , GAD S.
09461803	Not	093	12/16/1999	METHOD AND APPARATUS TO	SHEAFFER,

	Issued			EXECUTE A MEMORY MAINTENANCE OPERATION DURING A SCREEN BLANKING INTERVAL	GAD S.
09348404	Not Issued	093	07/07/1999	MAPPING DESTINATION LOGICAL REGISTER TO PHYSICAL REGISTER STORING IMMEDIATE OR RENAMED SOURCE REGISTER OF MOVE INSTRUCTION AND USING MAPPING COUNTERS	SHEAFFER , GAD S.
08774659	5838941	150	12/30/1996	AN OUT-OF-ORDER SUPERSCALAR MICROPROCESSOR WITH A RENAMING DEVICE THAT MAPS INSTRUCTIONS FROM MEMORY TO REGISTERS	SHEAFFER , GAD S.
08658933	5818745	150	05/31/1996	COMPUTER FOR PERFORMING NON-RESTORING DIVISION	SHEAFFER , GAD S.
08623486	5909573	150	03/28/1996	METHOD OF BRANCH PREDICTION USING LOOP COUNTERS	SHEAFFER , GAD S.
08621136	5790822	150	03/21/1996	METHOD AND APPARATUS FOR PROVIDING A RE-ORDERED INSTRUCTION CACHE IN A PIPELINED MICROPROCESSOR	SHEAFFER, GAD S.
<u>09896423</u>	Not Issued	030	06/29/2001	METHOD AND APPARATUS FOR ATTACHING ACCELERATOR HARDWARE CONTAINING INTERNAL STATE TO A PROCESSING CORE	SHEAFFER, GAD
10107260	Not Issued	030	03/28/2002	ADDRESSING MODES AND/OR INSTRUCTIONS AND/OR OPERATING MODES FOR ON-THE-FLY, PRECISION ADJUSTMENT OF PACKED DATA	SHEAFFER, GAD
10107259	Not Issued	030	03/28/2002	N-WIDE ADD-COMPARE-SELECT INSTRUCTION	SHEAFFER, GAD
10331608	Not Issued	020	12/31/2002	WIDELY ACCESSIBLE PROCESSOR REGISTER FILE AND METHOD FOR USE	SHEAFFER, GAD
10107261	Not Issued	020	03/28/2002	2N- WAY MAX/MIN INSTRUCTIONS USING N-STAGE 2- WAY MAX/MIN BLOCKS	SHEAFFER, GAD
10107262	Not	030	03/28/2002	ADDRESSING MODE AND/OR	SHEAFFER,

	Issued			INSTRUCTION FOR PROVIDING SINE & COSINE VALUE PAIRS	GAD
10107257	Not Issued	030	03/28/2002	METHOD AND APPARATUS FOR IMPLEMENTING SINGLE/DUAL PACKED MULTI-WAY ADDITION INSTRUCTIONS HAVING ACCUMULATION OPTIONS	SHEAFFER, GAD
10321573	Not Issued	030	12/18/2002	VARIABLE WIDTH, AT LEAST SIX-WAY ADDITION/ACCUMULATION INSTRUCTIONS	SHEAFFER, GAD
10107258	Not Issued	030	03/28/2002	COMBINED ADDITION/SUBTRACTION INSTRUCTION WITH A FLEXIBLE AND DYNAMIC SOURCE SELECTION MECHANISM	SHEAFFER, GAD
10107266	Not Issued	030	03/28/2002	MULTI-WAY SELECT INSTRUCTIONS USING ACCUMULATED CONDITION CODES	SHEAFFER, GAD
09454266	6351802	150	12/03/1999	METHOD AND APPARATUS FOR CONSTRUCTING A PRE-SCHEDULED INSTRUCTION CACHE	SHEAFFER, GAD S.
09489072	Not Issued	030	01/21/2000	SYSTEM AND METHOD FOR HISTORY-BASED BRANCH PREDICTIONS WITH MISSING ELEMENTS	SHEAFFER, GAD S.
10320627	Not Issued	030	12/17/2002	METHOD AND SYSTEM TO OVERLAP POINTER LOAD CACHE MISSES	SHEAFFER, GAD S.
10062143	Not Issued	030	01/31/2002	MULTIPLY-ACCUMULATE ACCELERATOR WITH DATA RE-USE	SHEAFFER, GAD S.
10185199	Not Issued	030	06/26/2002	METHOD FOR PERFORMING COMPLEX NUMBER MULTIPLICATION AND FAST FOURIER	SHEAFFER, GAD S.
10330971	Not Issued	019	12/26/2002	ELIMINATING REGISTER READS AND WRITES IN A SCHEDULED INSTRUCTION CACHE	SHEAFFER, GAD S.
10176020	Not Issued	030	06/19/2002	INSTRUCTION SET EXTENSION USING OPERAND BEARING NOP INSTRUCTIONS	SHEAFFER, GAD S.

10144538	Not Issued	030	05/10/2002	METHOD AND SYSTEM TO PERFORM COMPLEX NUMBER MULTIPLICATIONS AND CALCULATIONS	SHEAFFER, GAD S.
10112969	Not Issued	020	03/29/2002	ADD-COMPARE-SELECT ACCELERATOR USING PRE-COMPARE-SELECT-ADD OPERATION	SHEAFFER, GAD S.
09675819	Not Issued	030	09/29/2000	REDUCING THE LENGTH OF LOWER LEVEL INSTRUCTIONS BY SPLITTING AND RECOMBINING AN IMMEDIATE	SHEAFFER, GAD S.
09608159	Not Issued	030	06/30/2000	METHOD AND APPARATUS FOR AN INTEGRATED CIRCUIT HAVING FLEXIBLE-RATIO FREQUENCY DOMAIN CROSS-OVERS	SHEAFFER, GAD S.
09540432	Not Issued	041	03/31/2000	SYSTEM AND METHOD FOR PRE-STEERED INSTRUCTION CACHE	SHEAFFER, GAD S.

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